

ABSTRACT OF THE DISCLOSURE

A system may include several nodes coupled by an inter-node network. Each node may include an active device, a memory, an interface coupled by an address
5 network. The interface included in one of the nodes may receive from another node a coherency message requesting write access to a coherency unit. In response to receiving the coherency message, the interface is configured to send an address packet on its node's address network. The interface is configured to encode the node identification of the other node in the address packet. In response to the address packet, a memory in the node
10 is configured to update an indication associated with the coherency unit to indicate that the coherency unit is in a modified global access state in the other node. An active device in the node is configured to invalidate an access right to the coherency unit in response to the address packet.